# ENEE446 Digital Computer Design, Spring 2013

**Lectures:** TTh 2:00 - 3:15.

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**Text:** J.L. Hennessy and D.A. Patterson. Computer Architecture a Quantitative Approach, Fifth (!) Edition. Morgan-Kaufmann, San Francisco, 2012.
**References for some of the enrichment material:** 1.The Future of Computing Performance: Game Over or Next Level? Samuel H. Fuller and Lynette I. Millett, Editors; Committee on Sustaining Growth in Computing Performance; National Research Council, 2011.
2. U. Vishkin. Using simple abstraction to reinvent of computing for parallelism. Communications of the ACM 54,1 (January 2011), 75-85.

**Prerequisite:** A course in computer organization.
**Prerequisite knowledge:** Students should be familiar with at least one assembly language and one high level programming language such as C.

The **midterm exam** will be on Thursday, March 14, in class.

The **final exam** will <u>most likely</u> be at 10:30–12:30 on Wednesday, May 15.

**Course objectives** The main objective of the course is to introduce the basic concepts in contemporary computer architecture, including instruction sets, pipelining, advanced processor design, memory hierarchy, and storage systems. The emphasis will be on quantitative evaluation of various design issues Specific examples from current microprocessors will be given.

The discussion will also seek to use the material presented in order to advance the following definition of computer engineering: Theory, algorithms, software, architecture, tools, applications and practice in computing that can be methodically validated and their validation, whether analytical, experimental, or a combination thereof.

**Course description** The course will cover the following: Principles of computer design; cost/performance of design options; processor design. instruction set design and implementation; pipelining and instruction-level parallelism; floating-point arithmetic; memory-hierarchy design; caches; main memory; virtual memory; input/output design and performance measures; types of I/O devices; connections of I/O to CPU and main memory; last, but not least: the increasing role of parallelism from fine-grained to coarse-grained, what forms of parallelisms appear to be easier for programmers and the often missed critical importance of bandwidth (among processors, or between processors and memories).

The text is the basis for the course and is the backbone for the organization of the course. In particular, **no class is over till you went home, read the material in the text, and reached a critical understanding of it**.

Please see more on the teaching approach later.

## Grading

10% homework. Each student is required to submit at least 80% of the written homework.

Up to 15% quizzes whose date will not be preannounced.

Up to 40% midterm exam.

Up to 50% final exam. If your grade for the final exam is higher than your midterm grade, the final exam grade can be up to 90% of your final grade.

Note: The weight for quizzes will reduce the weight for the midterm and final, but will keep the ratio between them.


## Tentative schedule

Chapter 1: weeks 1-2.

Chapter 2: weeks 2-3.

Chapter 3: weeks 4-7.

Chapter 4: weeks 8-9.

Chapter 5: weeks 10-11.

Chapter 6: week 12.

Enrichment material: considerable amount of enrichment material will be covered. This may delay the tentative schedule above.

## Documented disability

If you have a documented disability and wish to discuss academic accommodations with me, please contact me as soon as possible.

## A textbook-centered teaching approach - a rationale

**Objective of course**   The main objective of the course is to reach a *critical understanding* of the textbook information by the students.
It is also a goal of the course to improve your ability to understand such information on your own, in the future.

**Background** We introduced this particular textbook to ENEE446 since: (i) it is very informative in conveying details of up to 20011-12 updates in state-of-the-art computer architecture, and (ii) it is commonly used at the top computer science and engineering Programs. However, I find that some parts of the textbook are unusually verbose and overly opinionated. Also, the level of detail often exceeds the level appropriate for class.

The main objective for the classroom presentation is to overcome these hurdles towards the main objective of the course. It is particularly challenging for a reader to locate the "punch lines". Also, students need to be equipped with a lot of background data and dissenting opinions in order to reach a point where they will be able to critically evaluate for themselves opinions expressed in the book, and form an independent opinion (or at least understand the pros and cons of the various approaches).

To get the most out of this teaching method students need to remember:
**No class is over till you went home, read the material in the text, and reached a critical understanding of it**. It is also expected that you prepare for class by reading the material that will be discussed.

You may wonder: *who uses similar methods?*
Quite a few liberal arts programs are based on studying directly from sources, such as the original texts written by great thinkers. There, understanding texts is the main thing and the role of a teacher, or tutor, is to get the students to derive as much as possible from the text. The teacher's goal is to add to the text, but at the same time not "temper" with the access of the students to the source itself. Adding needed background, and questioning of assertions, are often more important to the students than helping reading the text, which they can often do reasonably well on their own. Indeed, this undergraduate/graduate course: (i) tries to convey trends in computer architecture (which is always a debatable issue), (ii) covers an unusually broad range of issues, and (iii) requires different background discussions in the context of its various chapters.

As a final motivation, I would like to note that learning how to think and process content is as much a goal of university education as absorbing content. As an instructor, I will operate as a knowledge agent more than a content supplier (a role played by the text, and other literature, including on the Web). Graduate study (and work as professional engineers) is often a do-it-yourself process; however, you can, and should, expect help and guidance.

The chair of our department distributed a while ago an article entitled Thoughts on Teaching our Engineers-To-Be, by MIT Professor Warren Seering. The article points out

that: Frequently students express serious reservations and sometimes great displeasure when faced with an engineering problem that requires clarification of objectives, synthesis, the making of assumptions, reduction to a form that can be solved, and interpretation of the result.

However, the main point of the article is that teaching how to cope with such an open-ended engineering problem may be the single most neglected issue in current engineering education. My understanding of the educational objectives of ENEE446 is that it should play a key role in educating computer engineering students to cope with open ended problems through the process of digesting the textbook, as will be demonstrated in class. My request from you is to approach this with an open mind, and realize that the reservations and displeasures you may have may be neither your fault NOR MINE. Just take it easy and do your best to get the most out of this important class.

Lastly, it may also be informative for you to know that the other part of my job as a Professor is doing research; there, I am also trying to be a content producer.